

FPT 2011 : Call for Papers

<http://www.icfpt.org>

FPT'11
Delhi

2011 International Conference on Field-Programmable Technology
New Delhi, India **12-14 December 2011**

FPT is the premier conference in the Asia-Pacific region on field-programmable technologies including reconfigurable computing devices and systems containing such components. Field programmable devices offer the flexibility of software with the performance of hardware. The development and application of field-programmable technology have become important topics of research. Field-programmable components are widely applied, such as in high-performance computing systems, embedded and low-power control instruments, mobile communications, rapid prototyping and product emulation.

Submissions are solicited on new research results and detailed tutorial expositions related to field-programmable technologies, including but not limited to:

Tools and Design techniques for field-programmable technology including placement, routing, synthesis, verification, debugging, run-time support, technology mapping, partitioning, parallelization, timing optimization, design and run-time environments, languages and modeling techniques, provably correct development, intellectual property core based design, variation-aware design, domain-specific development, hardware/software co-design.

Architectures for field-programmable technology including field programmable gate arrays, complex programmable logic devices, coarse-grained reconfigurable arrays, field programmable interconnect, field programmable analogue arrays, field programmable arithmetic arrays, memory architectures, interface technologies, low-power techniques, adaptive devices, reconfigurable computing systems, high-performance reconfigurable systems, evolvable hardware and adaptive computing, fault tolerance and avoidance.

Device technology for field-programmable logic including programmable memories such as nonvolatile, dynamic and static memory cells and arrays, interconnect devices, circuits and switches, and emerging VLSI device technologies.

Applications of field-programmable technology including biomedical and scientific computation, network processors, real-time systems, rapid prototyping, hardware emulation, digital signal processing, interactive multimedia, machine vision, computer graphics, cryptography, robotics, manufacturing systems, embedded applications, low-power devices, evolvable and biologically-inspired hardware.

Education for field-programmable technology including courses, teaching and training experience, experiment equipment, design and applications.

Note that simply implementing an application using an FPGA is not sufficient to count as a research contribution. Application-based papers should emphasize novel design techniques or clearly articulated and measured system performance benefits.

Submissions

The program committee solicits papers describing original research and high quality half or full day tutorial expositions in field-programmable technology including, but not limited to, the areas of interest indicated above. High quality short papers are also solicited. Tutorials will precede the conference. Postgraduate research students are invited to submit short papers detailing their proposed research to be presented in a poster-based PhD forum.

In addition to the above, the organizers solicit contributions to the following :

- Special Session on Reconfigurable Computing in India: Applications and Design Methods
- Demonstration of Tools, Techniques and Applications
- Design Competition

Further details of these special sessions are available on the conference website :
<http://www.cse.iitd.ac.in/~icfpt11>

Papers must be prepared in PDF format using the template files provided and submitted electronically via the conference website. Full papers should not exceed 8 pages in length, while short papers should not exceed 4 pages. PhD forum papers are limited to 2 pages.

FPT uses a blind reviewing system. Manuscripts must not identify authors or their affiliations. Self-references should be blanked out. Papers that identify authors will NOT be considered.



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IMPORTANT DATES

Submission of regular papers and tutorial proposals: 8 June 2011

Submission of demo papers: 1 August 2011

Notification of acceptance: 10 August 2011

Final paper and registration due: 15 September 2011

Design competition papers due: 15 October 2011

ORGANIZING COMMITTEE

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