

February 25

**8:45 – 9:00 AM Welcome**

**9:00 AM – 10:00 AM Keynote**

Prof. David Rosenblum, University College, London, **Software System Scalability: Concepts and Techniques**

**10 – 10:30: Coffee break**

**10:30 – 12:50: ICSE and FSE best papers**

**FSE Best paper 1:** Bettenburg, N., Just, S., Schröter, A., Weiss, C., Premraj, R., and Zimmermann, T, **What makes a good bug report?**,

**FSE Best paper 2.** Raimondi, F., Skene, J., and Emmerich, W, **Efficient online monitoring of web-service SLAs**

**ICSE Best Paper 1.** G. Xu, and A. Rountev, **Precise memory leak detection for java software using container profiling**

**ICSE Best Paper 2.** Ko, A. J. and Myers, B. A, **Debugging reinvented: asking and answering why and why not questions about program behavior,**

**12:50 – 2:00: Lunch**

**2:00 – 3:30: Research Papers I**

Gautam Shroff, Prem Devanbu and Puneet Agarwal. **Instant Multi-Tier Web Applications without Tears**

Vinay Kulkarni and Sreedhar Reddy. **Extending AOP with models**

Nitu. **Configurability in Software as a Service (SaaS) applications**

**3:30 – 4:00: Coffee Break**

**4:00 – 5:30: Research Papers II**

Sujit Chakrabarti and Srihari Sukumaran. **Using Spreadsheets for Finite State Modelling**

Prateek Jain, Kunal Verma, Alex Kass and Reymonrod Vasquez. **Automated Review of Natural Language Requirements Documents: Generating Useful Warnings with User-extensible Glossaries Driving a Simple State Machine**

Fabricio Fernandes, Robin Passama and Jean-Claude Royer. **Event Strictness for Components with Complex Bindings**

**5:45 – 6:30: Short presentations from sponsors**

**7:00 onwards: Dinner**

February 26

**9:00 – 10:00: Keynote**

Vijaykumar Ivaturi, CTO, Wipro, **Engineering global solutions from India – Opportunities and Challenges.**

**10:00 – 11:00: Research Papers III**

Rakesh Shukla. **Deriving Parameter Characteristics**

Philippe Dugerdil and Sébastien Jossi. **Computing Dynamic Clusters**

**11 – 11:30: Coffee Break**

**11:30 – 1:00: Experience reports**

Nagesh Mukundarao. **Challenges in Execution of Outsourcing Contracts**

Anuj Kumar and Gaurav Sengar. **Design and Implementation of Micro Caching**

Ajit Shenvi. **Defect Prevention with Orthogonal Defect Classification**

Kanaklata Narayan, Udayan Banerjee and Shantha Kumar. **Performance Optimization of SOA based AJAX Application**

Lavanya Abraham. **People issues in Software Engineering**

Stamatia-Ann Katriou and Evangelos Tolia. **From Twin Training to Pair Programming**

**1:00 – 2:00: Lunch**

**2:00 – 3:30: Research Papers IV**

Puneet Bhateja. **Grammar based asynchronous testing**

Balbir Barn. **On the evaluation of reference models for software engineering practice**

Olly Gotel, Vidya Kulkarni, Des Phal, Moniphal Say, Christelle Scharff and Thanwadee Sunetnanta. **Evolving an Infrastructure for Engineering, Communication, Project Management and Socialization to Facilitate Student Global Software Development Projects**

**3:30 – 4:00: Coffee Break**

**4:00 – 5:30: Poster Session**

Ratneshwer Gupta. **A Maturity Model for CBSE**

Prasad Bokil, Priyanka Darke, Ulka Shrotri and R Venkatesh. **Automatic generation of test-data to satisfy MCDC**

Frank Salger, Stefan Sauer and Gregor Engels. **An Integrated Quality Assurance Framework for Large Software Specifications**

Ananya Kanjilal, Sabnam Sengupta and Swapan Bhattacharya. **Analysis of Complexity of Requirements: A Metrics based Approach**

Kiran Kumar and T.V. Prabhakar. **Quality Attribute Game: A Game Theory based Technique for Software Architecture Design**

Pushpendra Singh and Banshi Chaudhary. **A Formal Model of Design-Patterns Based Design**

Indranil saha, Kuntal Chakraborty, Suman Roy, VishnuVardhan Reddy, Venkatappaiah Kurapati and Vishesh Sharma. **A Reverse Engineering of C Programs to Simulink Models with Conformance Testing**

Kirti Garg and Vasudeva Varma. **An Effective Learning Environment for Teaching Problem Solving in Software Architecture**

D Pranith Kumar, Anchal Nema and Rajeev Kumar. **Hybrid Analysis of Executables to Detect Security Vulnerabilities**

Mohsen Asadi and Raman Ramsin. **Method Engineering Process Patterns**

**5:45 – 6:30: Panel Discussion**